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NPTEL (<https://swayam.gov.in/explorer?ncCode=NPTEL>) » Microprocessors And Microcontrollers (course)

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Course outline

About NPTEL ()

How does an
NPTEL online
course work?
()

Week 0 : ()

Week 1 ()

Week 2 ()

Week 3 ()

Week 4 ()

Week 5 ()

Week 6 ()

Week 7 ()

Week 8 ()

Week 9 ()

Week 12 : Assignment 12

The due date for submitting this assignment has passed.

Due on 2025-04-16, 23:59 IST.

As per our records you have not submitted this assignment.

1)

1 point

Find the output current (I_{out}) of DAC0808(Digital to analog converter) for following conditions:

For binary input $(10100000)_2$ and $I_{ref} = 5\text{mA}$

- a) $78.4\ \mu\text{A}$
- b) $3.125\ \mu\text{A}$
- c) $3.125\ \text{mA}$
- d) $7.84\ \text{mA}$

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.

2)

Which pin decides the 8086 mode of operation?

1 point

- a) $\overline{MN}/\overline{MX}$
- b) $\overline{DT}/\overline{R}$
- c) ALE
- d) \overline{BHE} / S_7

- a.
- b.
- c.

Week 10 ()**Week 11 ()****Week 12 ()**

- Lecture 57 :
Interfacing
(Contd.) (unit?
unit=118&lesson
=119)
- Lecture 58 :
Interfacing
(Contd.) (unit?
unit=118&lesson
=120)
- Lecture 59 : 8086
(unit?
unit=118&lesson
=121)
- Lecture 60 : 8086
(Contd.) (unit?
unit=118&lesson
=122)
- Lecture 61 : 8086
(Contd.) (unit?
unit=118&lesson
=123)
- Lecture 62 : 8086
(Contd.) (unit?
unit=118&lesson
=124)
- Lecture 63 : 8086
(Contd.) (unit?
unit=118&lesson
=125)
- Lecture 64 : 8087
(unit?
unit=118&lesson
=126)
- Lecture Material
(unit?
unit=118&lesson
=127)
- Quiz: Week 12 :
Assignment 12
(assessment?
name=218)**
- Feedback Form
(unit?
unit=118&lesson
=169)
- Week 12 :
Assignment

d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

a.

3)

In 8086, the address bus is _____ bits wide.

- a) 16
- b) 8
- c) 20
- d) 32

a.

b.

c.

d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.

4)

Consider the following features of a CPU. Match them with 8086 or 8088 processor.

(i) 16-bit data bus

(ii) 8-bit data bus

(iii) BHE signal

(iv) 20-bit address bus

a) (i) – 8086, (ii) – 8088, (iii) – 8088, (iv) – 8086

b) (i) – 8086, (ii) – 8088, (iii) – 8086, (iv) – 8088

c) (i) – 8088, (ii) – 8086, (iii) – 8088, (iv) – 8086

d) (i) – 8086, (ii) – 8088, (iii) – 8086, (iv) – 8086

a.

b.

c.

d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

d.

1 point

1 point

Solution (unit?
unit=118&lesson
=205)

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5) Which register used as loop counter in 8086 microprocessors? 1 point

- a) AX
- b) BX
- c) CX
- d) DX

- a.
 b.
 c.
 d.

No, the answer is incorrect.
Score: 0

Accepted Answers:
c.

6) Which of the following is not present in Bus Interface Unit? 1 point

- a) Instruction queue
- b) Segment registers
- c) Instruction pointer
- d) Flag register

- a.
 b.
 c.
 d.

No, the answer is incorrect.
Score: 0

Accepted Answers:
d.

7) Calculate the physical address for Segment offset pair 7213: 5686. 1 point

- a) 0C899h
- b) 777B6h
- c) 776B6h
- d) 777B9h

- a.
 b.
 c.
 d.

No, the answer is incorrect.
Score: 0

Accepted Answers:
b.

8) 1 point

Which of the following address lines in an 8086 microprocessor are multiplexed with status signals

- a) $A_3 - A_0$
- b) $A_{19} - A_{16}$
- c) $A_{15} - A_0$
- d) $A_8 - A_0$

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

b.

9) **Instruction Queue size of 8086 microprocessor?** **1 point**

- a) 4 bytes
- b) 6 bytes
- c) 10 bytes
- d) 2 bytes

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

b.

10) **Co-processor can be attached with 8086 in** **1 point**

- a) Minimum mode
- b) Maximum mode
- c) Both Minimum and Maximum modes
- d) No mode

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

b.

11) **1 point**

The action performed by the following instruction of 8086:
MOV [1234h], AX is?

- a) Move contents of memory location 1234h to register AX
- b) Move the contents of register AX to memory offset 1234h
- c) Add contents at 1234h and AX
- d) Add contents of 1234h and AX and store the result in 1234h

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

b.

12) The WAIT instruction of 8086 checks the pin

1 point

- a) TEST
- b) BHE
- c) DEN
- d) HOLD

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

a.

13)

1 point

In MOVS instruction of 8086, the segment register for the destination is

- a) CS
- b) DS
- c) ES
- d) SS

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.

14) The ALU in EU of 8086 is

1 point

- a) 16 bit
- b) 20 bit
- c) 8 bit
- d) 24 bit

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

a.

15)

1 point

In 8086, data access by which register uses SS as the default segment register

- a) SP
- b) BP
- c) Both SP and BP
- d) DI

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.