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[NPTEL \(https://swayam.gov.in/explorer?ncCode=NPTEL\)](https://swayam.gov.in/explorer?ncCode=NPTEL) » Microprocessors And Microcontrollers (course)


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Course
outline

About
NPTEL ()

How does an
NPTEL
online
course
work? ()

Week 0 : ()

Week 1 ()

Week 2 ()

- Lecture 7 :
8085
Microprocesso
rs (unit?
unit=28&lesso
n=29)

Week 2 : Assignment 2

The due date for submitting this assignment has passed.

Due on 2025-02-05, 23:59 IST.

Assignment submitted on 2025-02-04, 11:40 IST

1)

1 point

Which of the following is correct.

- a) A microprocessor contains ALU, flash memory and control units
- b) A microprocessor contains ALU, registers and control units
- c) A microcontroller contains ALU and control units only
- d) A microprocessor contains ALU only

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

b.

2)

1 MB memory equivalent to

- a) 1024 bits
- b) 1024 bytes
- c) 1024 KB
- d) 1024 GB

1 point

Lecture 8 :
8085
Microprocesso
rs (Contd.)
(unit?
unit=28&lesso
n=30)

Lecture 9 :
8085
Microprocesso
rs (Contd.)
(unit?
unit=28&lesso
n=31)

Lecture 10 :
8085
Microprocesso
rs (Contd.)
(unit?
unit=28&lesso
n=32)

Lecture 11 :
8085
Microprocesso
rs (Contd.)
(unit?
unit=28&lesso
n=33)

Lecture
Material (unit?
unit=28&lesso
n=34)

Quiz: Week 2
: Assignment
2
(assessment?
name=208)

Feedback
Form (unit?
unit=28&lesso
n=147)

Assignment 2
Solution (unit?
unit=28&lesso
n=185)

Week 3 ()

Week 4 ()

Week 5 ()

- a.
 b.
 c.
 d.

Yes, the answer is correct.
Score: 1

Accepted Answers:

c.

3) **8085 microprocessor has how many pins**

1 point

- a) 20
b) 60
c) 40
d) 30

- a.
 b.
 c.
 d.

Yes, the answer is correct.
Score: 1

Accepted Answers:

c.

4) **The program counter in a 8085 micro-processor is a 16-bit register, because**

1 point

- a) It counts 16-bits at a time
b) There are 16 address lines
c) It can fetch two 8 bit data at a time
d) none of the above

- a.
 b.
 c.
 d.

Yes, the answer is correct.
Score: 1

Accepted Answers:

b.

5)

1 point

[Week 6 \(\)](#)[Week 7 \(\)](#)[Week 8 \(\)](#)[Week 9 \(\)](#)[Week 10 \(\)](#)[Week 11 \(\)](#)[Week 12 \(\)](#)[Download Videos \(\)](#)[Text Transcripts \(\)](#)[Books \(\)](#)

In Intel 8085 microprocessor ALE signal is made high to

- a) To fetch the instruction
- b) To increment the program counter
- c) Enable the data bus to be used as higher order address bus
- d) Enable the data bus to be used as low order address bus

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

d.

6)

8085 microprocessor flag register contains

- a) Address of the next instruction to be fetch
- b) S,Z,AC,P,CY flags
- c) data required to execute current instruction
- d) starting address of the program

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

b.

7)

The clock speed of 8085 is

- a) 10 MHz
- b) 2.5 THz
- c) 8 MHz
- d) 3.125 MHz

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

d.

1 point

1 point

8)

1 point

The register which holds the information about the nature of results of arithmetic and logic operations is called as

- a) Flag Register
- b) Accumulator
- c) Program Counter
- d) Register B

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

a.

9)

1 point

CMA instruction

- a) Decrement the contents of the accumulator
- b) Performs 1's complement of the accumulator contents
- c) Performs 2's complement of the accumulator contents
- d) Increment the contents of the accumulator

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

b.

10)

1 point

INX BC instruction

- a) Increment the contents of register B by one
- b) Increment the contents of register C by one
- c) Increment the contents of register pair BC by one
- d) Contents of C is incremented but contents of B is un changed

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

c.

11) **If A register of 8085 contains 4EH, the parity flag is** **1 point**

- a) Reset
- b) Set
- c) Not decidable
- d) Don't care

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

b.

12) **A program that uses mnemonics is called** **1 point**

- a) Object program
- b) Fetch cycle
- c) Assembly language
- d) Micro instruction

- a.
- b.
- c.
- d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

c.

13) **1 point**

In an 8085 processor, suppose the accumulator content is FFH and the carry flag is 0. What will be the content of the accumulator after RAL and RLC instructions?

- a) Both FFH
- b) Both FEH
- c) RAL - FFH, RLC - FEH
- d) RAL - FEH, RLC - FFH

- a.

- b.
 c.
 d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

d.

14)

1 point

Which register pair of 8085 is NOT accepted in LDAX instruction?

- a. BC
b. DE
c. HL
d. All pairs are acceptable

- a.
 b.
 c.
 d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.

15)

1 point

After the execution of the instruction XRA A the contents of A, carry and zero flags are respectively

- a) A = 00, CY = 1, Z = 1
b) A = 00, CY = 0, Z = 0
c) A = FF, CY = 1, Z = 0
d) A = 00, CY = 0, Z = 1

- a.
 b.
 c.
 d.

Yes, the answer is correct.

Score: 1

Accepted Answers:

d.

