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[NPTEL \(https://swayam.gov.in/explorer?ncCode=NPTEL\)](https://swayam.gov.in/explorer?ncCode=NPTEL) » Microprocessors And Microcontrollers (course)


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Course  
outline

About NPTEL  
( )

How does an  
NPTEL online  
course work?  
( )

Week 0 : ( )

Week 1 ( )

Week 2 ( )

Week 3 ( )

Week 4 ( )

Week 5 ( )

Lecture 22:  
8085  
Microprocessor

## Week 5 : Assignment 5

The due date for submitting this assignment has passed.

**Due on 2025-02-26, 23:59 IST.**

As per our records you have not submitted this assignment.

1)

1 point

A microcontroller at-least should consist of:

- a) RAM, ROM, I/O devices, serial and parallel ports and timers
- b) CPU, RAM, I/O devices, serial and parallel ports and timers
- c) CPU, RAM, ROM, I/O devices, serial and parallel ports and timers
- d) CPU, ROM, I/O devices and timers

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.

s (Contd.)  
(unit?  
unit=55&lesson  
=56)

Lecture 23:  
8051  
Microcontroller  
(unit?  
unit=55&lesson  
=57)

Lecture 24:  
8051  
Microcontroller(  
Contd.) (unit?  
unit=55&lesson  
=58)

Lecture 25:  
8051  
Microcontroller  
(Contd.) (unit?  
unit=55&lesson  
=59)

Lecture 26:  
8051  
Microcontroller(  
Contd.) (unit?  
unit=55&lesson  
=60)

Lecture  
Material (unit?  
unit=55&lesson  
=61)

**Quiz: Week 5 :  
Assignment 5  
(assessment?  
name=211)**

Feedback Form  
(unit?  
unit=55&lesson  
=152)

Week 05:  
Assignment  
Solution (unit?  
unit=55&lesson  
=193)

**Week 6 ()**

**Week 7 ()**

**Week 8 ()**

2)

What is the size internal ROM memory of 8051 Microcontroller?

1 point

- a) 128 bits
- b) 128 bytes
- c) 4K bits
- d) 4K bytes

- a.
- b.
- c.
- d.

No, the answer is incorrect.  
Score: 0

Accepted Answers:

d.

3)

Number of pins present in 8051 Microcontroller?

1 point

- a) 20
- b) 30
- c) 40
- d) 80

- a.
- b.
- c.
- d.

No, the answer is incorrect.  
Score: 0

Accepted Answers:

c.

4)

Which of the following is true in the context of 8051 microcontroller?

1 point

- a) I/O port 0 is used as 8 bit R/W general purpose input output operations
- b) I/O port 1 is used as an address bus for external memory design
- c) I/O port 2 is used for internal timers and external interrupts
- d) None of the above

- a.
- b.
- c.
- d.

[Week 9 \(\)](#)[Week 10 \(\)](#)[Week 11 \(\)](#)[Week 12 \(\)](#)[Download Videos \(\)](#)[Text Transcripts \(\)](#)[Books \(\)](#)

No, the answer is incorrect.

Score: 0

Accepted Answers:

a.

5)

**1 point**

What is the time taken by one machine cycle if crystal frequency is 20MHz?

- a) 1.085 micro seconds
- b) 0.60 micro seconds
- c) 0.75 micro seconds
- d) 1 micro seconds

a.

b.

c.

d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

b.

6)

**1 point**

The contents of SP register after RESET?

- a) 06H
- b) 00H
- c) 0AH
- d) 07H

a.

b.

c.

d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

d.

7)

**1 point**

Which pin of port 3 is has an alternative function as write control signal for external da memory?

- a) P3.0
- b) P3.3
- c) P3.6
- d) P3.7

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.

8)

**1 point**

The SP is of \_\_\_ wide register. And this may be defined anywhere in the \_\_\_\_\_.

- a) 8 byte, on-chip 128 byte RAM.
- b) 8 bit, on chip 256 byte RAM.
- c) 16 bit, on-chip 128 byte ROM.
- d) 8 bit, on chip 128 byte RAM.

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

d.

9)

**1 point**

Match the following:

- |         |                                       |
|---------|---------------------------------------|
| 1) TCON | i) contains status information        |
| 2) SBUF | ii) timer / counter control register. |
| 3) TMOD | iii) idle bit, power down bit         |
| 4) PSW  | iv) serial data buffer for Tx and Rx. |
| 5) PCON | v) timer/ counter modes of operation. |

- a) 1->ii, 2->iv, 3->v, 4->i, 5->iii.
- b) 1->i, 2->v, 3->iv, 4->iii, 5->ii.
- c) 1->v, 2->iii, 3->ii, 4->iv, 5->i.
- d) 1->iii, 2->ii, 3->i, 4->v, 5->iv.

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

a.

10) How many 16 bit registers present in 8051 microcontroller? **1 point**

a) 1

b) 2

c) 3

d) 4

a.

b.

c.

d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

b.

11) Which of the following is true, if the contents of PSW is 16H? **1 point**

Which of the following is true, if the contents of PSW is 16H?

a) Program uses registers from bank3

b) Carry flag set to 1

c) program uses registers from bank2

d) Program uses registers from bank0

a.

b.

c.

d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

c.

- 12) **1 point**
- An alternate function of port pin P3.0 in the 8051 is:
- a) Timer 0 external input
  - b) Serial input port
  - c) External data memory write strobe
  - d) External data memory read strobe

- a.
- b.
- c.
- d.

No, the answer is incorrect.  
Score: 0

Accepted Answers:

b.

- 13) **1 point**
- How many 16 bit timers/counters present in 8051 microcontroller?
- a) 8
  - b) 4
  - c) 2
  - d) 1

- a.
- b.
- c.
- d.

No, the answer is incorrect.  
Score: 0

Accepted Answers:

c.

- 14) **1 point**
- Which of the following Flag is not present in PSW?
- a) Carry Flag (CY)
  - b) Parity Flag (P)
  - c) Overflow Flag (OV)
  - d) GF0

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

*d.*

15)

When PUSH instruction is executed.

**1 point**

- a) The SP is incremented by one
- b) The SP is incremented by two
- c) The SP is decremented by one
- d) The SP is decremented by two

- a.
- b.
- c.
- d.

No, the answer is incorrect.

Score: 0

Accepted Answers:

*a.*